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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

NANJO, Ryota et al.

Group Art Unit: 2822

Serial No.: 10/084,367

Examiner: Toniae M. Thomas

Filed: February 28, 2002

P.T.O. Confirmation No.: 9203

For: SEMICONDUCTOR DEVICE AND ITS MANUFACTURE METHOD

**AMENDMENT AND RESPONSE  
TO ELECTION OF SPECIES REQUIREMENT**

Commissioner for Patents  
Washington, D.C. 20231

March 3, 2003

Sir:

This is in response to the Office Action of January 31, 2003, requiring an election among various species of the subject invention under the provisions of 35 USC § 121.

**IN THE CLAIMS:**

Amend the claims as follows:

1. (Amended) A method of manufacturing a semiconductor device according to claim 10, wherein
- said step (b) forms first and second gate electrodes in respective partial areas of said first and second regions;
- said step (c) implants impurities of the second conductivity type into a surface layer in said second region on both sides of said second gate electrode;